

## Online Technical Support Repositories



The screenshot shows a navigation bar with buttons for Home, Technology, Products, News & Events, Consortium, Member Login, and Become a Member. Below the navigation bar is a dark blue banner with the text "General Technical Support Database" and "Accessible by all members (Promoter, Contributor, Adopter and Academic)".

The General Technical Support Database contains a selection of general technical support documentation and software of invaluable contribution to HyperTransport product development, validation and rapid time-to-market. The General Technical Support Database includes **HyperTransport specifications, design and test guides, compatibility checklists, simulation and compliance tools**, as listed below.

### [HT Specifications](#)

By **HyperTransport Consortium**

All revisions of the following HyperTransport Specifications:

- HT Link
- HT Node and Mezzanine Connectors **New!**
- HTX and HTX3 Slot Connectors
- Design-Under-Test (DUT) Connector

### [HT Design Guide](#)

By **HyperTransport Consortium**

Basic HT design guide information.

### [HT 1.0 and HT 3.0 Bus Functional Model](#)

By **AMD**

HTC members can access the complete source code for the HT 1.0 and HT 3.0 Bus Functional Model simulation tools. To download, the member will be required to review and accept AMD's software licensing terms and conditions.

### [Generic HTX BIOS Guidelines](#)






By **HyperTransport Consortium**

Guidelines for a generic HTX BIOS implementation, developed for HTX-based system and subsystem manufacturers, as well as independent BIOS development vendors.

### [jEye: A Statistical Channel Compliance Tool](#)

By **AMD**

This white paper describes how jEye provides channel analysis for HyperTransport 3.0 links. TWG member companies can also access the companion jEye self-extracting installation package - in ZIP archive executable format for Windows and with example setups - in the Technical Working Group (TWG) Documents Database (see below).

-  [HT 1.0 Electrical Compatibility Measurements](#)  
By HyperTransport Consortium  
This document describes the procedures required to certify that devices are compliant with the HyperTransport I/O Link specification 1.0.
-  [HT 1.0 Device Compatibility Checklist](#)  
By HyperTransport Consortium  
Tool for expediting compatibility verification with HyperTransport I/O Link specification 1.0.
-  [Sample Member HT Test Plan](#)  
By HyperTransport Consortium  
Sample HyperTransport 1.0 features test plan.
-  [Tools for HT Link Characterization](#)  
By HyperTransport Consortium  
General recommendations for the use of a logic analyzer for protocol validation, signal integrity characterization and validation of the HyperTransport link.
-  [Testing HT Links](#)  
By Agilent Technologies  
White paper background on testing HyperTransport 1.0 and 2.0 Links.



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## Extended Technical Support Database



Technical Working Group Repository

Accessible by Promoter and Contributor members

The Extended Technical Support Database contains a superset of advanced HyperTransport technical support material and tools that includes **in-progress specification proposals in process of review and voting by the Consortium's Technical Working Group (TWG) and a collection of in-depth design, testing & validation support documentation, guidelines and software** (listed below).

The combination of Basic and Extended Technical Support Database material offers high-level Consortium members not only **the most comprehensive HyperTransport development support platform**, but invaluable **advanced knowledge on HyperTransport technology developments to come** – an open window on the future of HyperTransport and a competitive business advantage over low-level Consortium members and the industry at large.

-  [HT Specifications Pipeline](#)  
By HyperTransport Consortium  
HyperTransport technology specification proposals/updates under development, discussion and/or approval by the Consortium's Technical Working Group.

-  [High Node Count HyperTransport Specification – Rel. 1.0](#)  
By [HyperTransport Consortium](#)  
HyperTransport protocol extensions that complements the HyperTransport Link specifications for the support of large number of computing nodes in scalable, non-coherent, shared-memory architectures.
-  [HT to Optics Conversion](#)  
By [HyperTransport Consortium](#)  
HyperTransport to Optics conversion guidelines and techniques.
-  [Method of HT Link Probing](#)  
By [FuturePlus](#)  
Guidelines and methodologies for probing the HyperTransport link and protocol.
-  [HT Characterization on Agilent 93000 Tester](#)  
By [Agilent Technologies](#)  
New test methodologies that address the challenges of testing high bandwidth interconnects like HyperTransport.
-  [HT Validation Test Suite - Manual](#)  
By [HyperTransport Consortium](#)  
This manual describes a useful tool for debugging and testing HT designs. It accompanies the HT Validation Software Test Suite (below).
-  [HT Validation Test Suite - Software](#)  
By [HyperTransport Consortium](#)  
A 120 MB zip file that includes the source, executables, and documentation for the HT Validation Test Suite Software.
-  [jEye Installation Package - Software](#)  
By [HyperTransport Consortium](#)  
Self-extracting installation package for HyperTransport 3.0 in ZIP archive executable format for Windows and with example setups. See also companion white paper "jEye - A Statistical Channel Compliance Tool" on the General Technical Document Database page.
-  [HT 2.0 Link Protocol Compatibility Checklist](#)  
By [HyperTransport Consortium](#)  
Tool for expediting compatibility verification with the HyperTransport 1.0 and 2.0 I/O Link specifications.
-  [HT 3.0 Validation and Compliance](#)  
By [Agilent Technologies](#)  
With data transfer rates up to 5.2Gb/s, HyperTransport 3.0 bus more than triples the maximum speed of HyperTransport 1.0 and supports faster data rate than even PCI Express Gen 2. Validation and compliance approaches must be taken to the next level as well. Concepts like fixture de-embedding must become an integral part of a test plan and toolset. This overview - presented by Agilent Technologies at the HTC-sponsored web seminar of May 17, 2007 – illustrates the main concepts and strategies towards an efficient validation and compliance testing of HyperTransport 3.0 silicon and systems.



## TWG Rules

By HyperTransport Consortium

TWG and Task Force guidelines, voting rules.



## TWG Contribution Form

By HyperTransport Consortium

Form used by TWG member companies to submit member-born, HT-related specification and/or enhancement proposals for possible standardization by the TWG and HTC.

