

For Immediate Release



Press Contact:

Erika Powelson
Powelson Communications
831-424-1811
email: erika@powelsoninc.com

Reader Contact:

Mario Cavalli
HyperTransport Consortium
925-968-0220
email: mario.cavalli@hypertransport.org

HyperTransport Consortium Elects New President

30-year Industry Veteran to Join Consortium's Executive Committee

SUNNYVALE, Calif., February 13, 2009 – The HyperTransport™ Technology Consortium, a non-profit organization dedicated to developing, promoting and licensing the industry's lowest latency, highest bandwidth interconnect technology, today announced the election of Mike Uhler, vice president of Accelerated Computing for AMD (NYSE: AMD) and former chief technology officer (CTO) of MIPS, as president of the Consortium.

"We are privileged to have such an experienced technology leader as part of our executive team," said Mario Cavalli, general manager of the HyperTransport Technology Consortium. "Mike's extensive expertise in advanced processor design and system architectures will be critical to the Consortium as we continue to advance the capability and reach of our technology. Furthermore, Mike's deep understanding of the high-performance computing market makes him the ideal candidate to help the Consortium capitalize on growth opportunities in this quickly expanding industry segment."

"The HyperTransport interconnect has experienced a high rate of adoption in a wide range of markets that have an insatiable need for increased performance and advanced feature sets, such as financial processing, real-time data analysis and high-performance server clustering," said Uhler, "I look forward to the opportunity to apply my technical and business background to help guide the Consortium and its innovative members on a path to further growth and success."

As a named inventor on 36 patents in computer architecture and design, Uhler has extensive experience developing advanced chip architectures and software for customers targeting high-performance markets. In his current role with AMD, he is responsible for driving future technological innovation, including the implementation of interconnect technology in next-generation designs. Previously as CTO of MIPS, Uhler worked directly with a variety of high-profile Consortium members, helping them to ease the integration of multiple IP cores into sophisticated IC designs. Prior to his role as CTO, he held various architecture and engineering responsibilities at MIPS. Uhler was also director of engineering at Silicon Graphics Inc.'s MIPS Group, and a senior consulting engineer at Digital Equipment Corp. Uhler holds a master's degree in computer science and a bachelor's degree in electrical engineering from the University of Arizona.

About the HyperTransport™ Technology Consortium

The HyperTransport Technology Consortium is a membership-based, non-profit organization that licenses, manages and promotes HyperTransport Technology. The HyperTransport Consortium was founded in 2001 by leading technology innovators like AMD, Apple, Broadcom, Cisco, NVIDIA, PMC-Sierra and Sun Microsystems and counts more than 60 industry-leading members worldwide, including AMD, Broadcom, Cisco, Cray, Dell, HP, IBM, Nvidia and Sun Microsystems. Consortium membership is

based on a yearly fee and it is open to companies interested in licensing the royalty-free use of HyperTransport technology and intellectual property. Consortium members have full access to the HyperTransport technical support database, they may attend Consortium meetings and events and may benefit from a variety of technical and business promotion services that HTC offers at no cost to its members.

To learn more about member benefits and how to become a Consortium member, please visit the Consortium Web site at <http://www.hypertransport.org>.

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